



CH
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Munir H. Nayfeh
Serial No.: 09/496,506
Conf. No.: 6344
Filed: February 2, 2000
For: SILICON NANOPARTICLE
FIELD EFFECT TRANSISTOR
AND TRANSISTOR MEMORY
DEVICE
Patent No.: 6,984,842
Issued: January 10, 2006
Art Unit: 2811
Examiner: Sara W. Crane

I hereby certify that this paper is being deposited with the
United States Postal Service as FIRST-CLASS mail in an
envelope addressed to: Attn: Certificate of Corrections
Branch, Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on this date.
March 29, 2006 *Am R. R.*
Date Registration No. 43,874
F-CLASS.WCM
Appr. February 20, 1998 Attorney for Applicant

Ce Certificate
APR 04 2006
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION UNDER RULE 322

Attn: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. § 1.322, patentees, through their attorneys,
respectfully request that a Certificate of Correction be issued in the above-referenced patent.

The errors occurred as a result of mistakes on the part of the Patent and
Trademark Office and the changes include the following:

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IN THE ISSUED PATENT:

Col. 4, line 62, before “is the”, please delete “e” and insert --e-- therefor.
(Specification, page 8, line 4).

Col. 5, line 28, after “($V_{n,k} - V_t$ ” and before “=” please insert an end parenthesis --)–. (Specification, page 8, line 26).

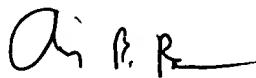
REMARKS

A Certificate of Correction incorporating the delineated changes is enclosed in duplicate herewith. Since the mistakes were on the part of the Patent and Trademark Office, a Certificate of Correction should be issued without expense to the patentees and such is respectfully requested.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By



Arik B. Ranson
Registration No. 43,874

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March 29, 2006

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,984,842
DATED : January 10, 2006
INVENTOR(S) : Nayfeh et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Issued Patent:

Col. 4, line 62, before "is the", please delete "e" and insert --e-- therefor.

Col. 5, line 28, after "(V_{n,k} - V_t)" and before "=" please insert an end parenthesis --).

MAILING ADDRESS OF SENDER:

PATENT NO 6,984,842

Arik B. Ranson
GREER, BURNS & CRAIN, LTD.
300 South Wacker Drive, 25th Floor
Chicago, IL 60606

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